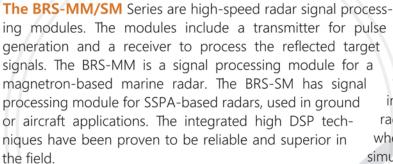


BRS Series – Radar Signal Processing Modules

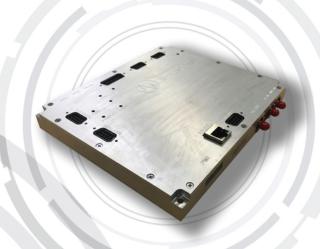
Features

- Signal processors for X-band magnetron & SSPA
- Up to 30MHz IF bandwidth, 100/125MHz IF
- Chirped NLFM pulse (BRS-SM)
- Auto frequency calibration (BRS-MM)
- IEC62388 compliant (BRS-MM)
- Frame-based pulse operation (BRS-SM)
- Frequency diversity & agility (BRS-MM)
- CFAR, STC, PRF stagger, pulse integration



Electrical features

Users can choose the intermediate frequency, either 100MHz, 125MHz or another specific frequency at the time of ordering. The signal bandwidth can be configured to be 30MHz max. It depends on the distance of the target. The input dynamic range is as wide as 50dB, 0 to -50dBm, regardless of the STC. The detection results are available for both analog and digital interfaces, and users can choose either one. The conventional analog interface consists of a set of signals, analog video, trigger, ACP and ARP. The digital interface is based on Configured to be 30MHz max. It depends on the distance of the target. The input dynamic range is as wide as 50dB, 0 to -50dBm, regardless of the STC. The detection results are available for both analog and digital interfaces, and users can choose either one. The conventional analog interface consists of a set of signals, analog video, trigger, ACP and ARP. The digital interface is based on compact protocol.



Signal processing

for pulse All key functional blocks are implemented ed target on a single integrated processor, minimizule for a ing the analog circuit. The BRS-SM invokes signal the NLFM pulse compression technique fround intended for application to SSPA-based technique radars. They also operate a frame-based pulse or in where multiple pulses of different lengths are simultaneously transmitted and received while the BRS-MM transmits a single pulse. The BRS-MM for magnetron-based marine radars has been fully tested under the conditions specified in IEC 62388. High DSP techniques applied include frequency diversity, CFAR, STC, PRF stagger, pulse integration, DDC and so on. The performance of those integrated in an ASDE has been tested in the field.

Supplementary functions

Besides the main features, the BRS-SM/SM series each have such useful functions as AFC, Static/Blanking MAP, pulse expansion, ARP/ACP/Trigger manipulation, BIT and so on.

Configurable registers

The configurable registers allow users to have easy access to the highly flexible combination of features.



Specifications for BRS-SM

Electrical specifications

Frequency (IF)	100/125MHz or else
Bandwidth	30MHz or less
Output power	-5dBm
Input dynamic range	50dB(0 ~-50dBm)
In-band spurious	50dBc
Out-band spurious	65dBc
Pulse operation	Short : CW
	Medium : NLFM(downward)
	Long : NLFM(upward)
	-
Dulas width	Short: 70ns
Pulse width	Medium : 4us
	Long : 13us
Clutter suppression	CA-CFAR, Static MAP
Signal strengthening	Pulse compression, pulse integration
Echo/interference	PRF Stagger, frequency
suppression	diversity, blanking MAP
Input regulation	STC(2CH, 0.1 ~4.5V)
Interface	RF : SMA Digital : DSUB37, RJ-45
Primary power	7VDC, 2.5A

Environmental specifications

Operating temperature	-20 ~ +50°C
Storage temperature	-40 ~ +85°C
Humidity	90%, non-condensing
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Dimensions & weight

Dimensions	180 X 160 X 30 mm ³
Weight	780a

Specifications for BRS-MM

Electrical specifications

Frequency (IF)	75MHz or else
Bandwidth	30MHz or less
Input dynamic range	50dB(0 ~ -50dBm)
Pulse operation	Pulsed CW
Pulse width	75ns, 0.25us, 1us
Clutter suppression	CA-CFAR
Signal strengthening	Pulse integration
Echo suppression	PRF stagger,sector blanking
Input regulation	STC(2CH, 0.1~4.5V)
Interface	RF : SMA Digital : DSUB37, RJ-45
Primary power	7VDC, 2.5A

Environmental specifications

Operating temperature	-20 ~ +50°C
Storage temperature	-40 ~ +85°C
Humidity	90%, non condensing

Dimensions & weight

Dimensions	180 X 160 X 30mm ³
Weight	780g

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